



# PI#15.4 TDC MVP Hardware Testing

**SPO-1759 TDC MVP1 Build 1 Testing PI15**

**SP-2478 TDC MVP1 Build 1 - Execute Test Plan (Part 2)**

**CIPA Team, NRC/MDA**

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National Research  
Council Canada

Conseil national  
de recherches Canada

Canada 

# Objective

## SPO-1759 TDC MVP1 Build 1 Testing PI15

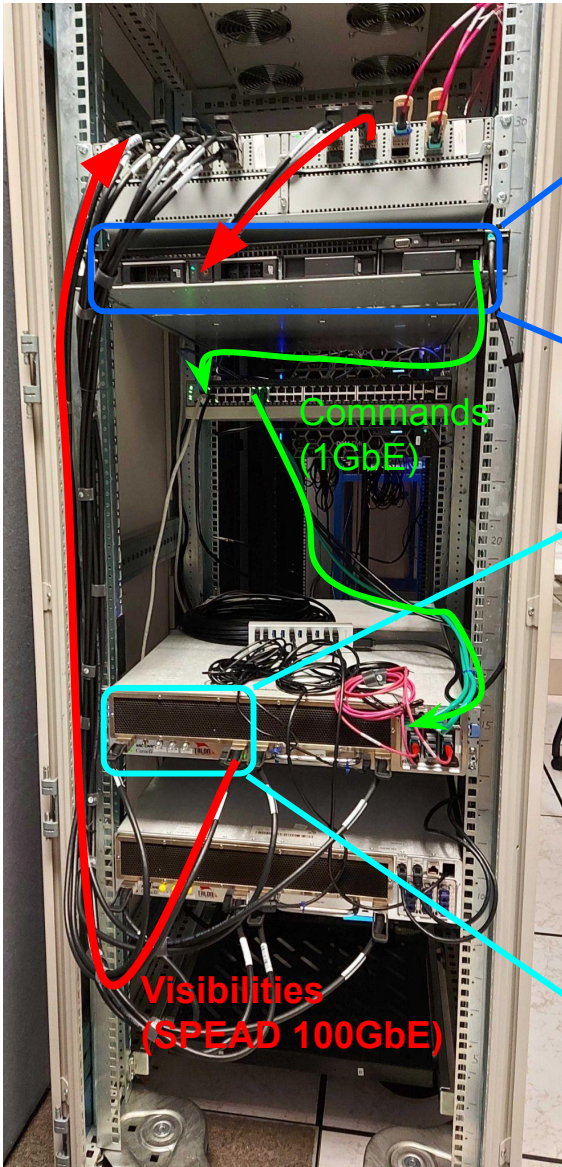
Execute TDC MVP1 signal chain test plan to verify that End-to-End signal processing is operating as expected for one receptor and one frequency slice.

### Key Results:

- KR1: VCC and fine channelizer gains can be controlled and the impact observed using diagnostic tools.
- KR2: Visibility packets are generated correctly by the signal chain and interpreted by the Visibility Analysis Tool.



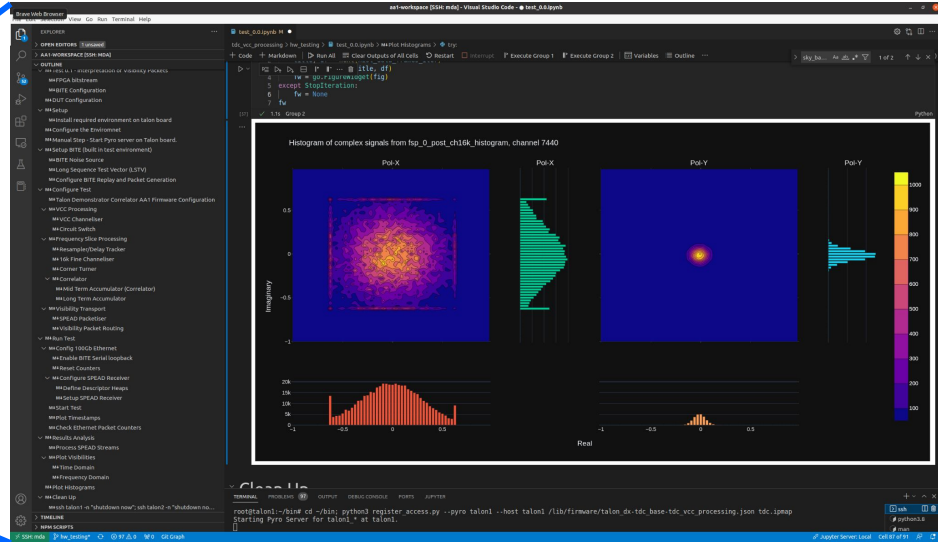
# TDC MVP FPGA Datapath



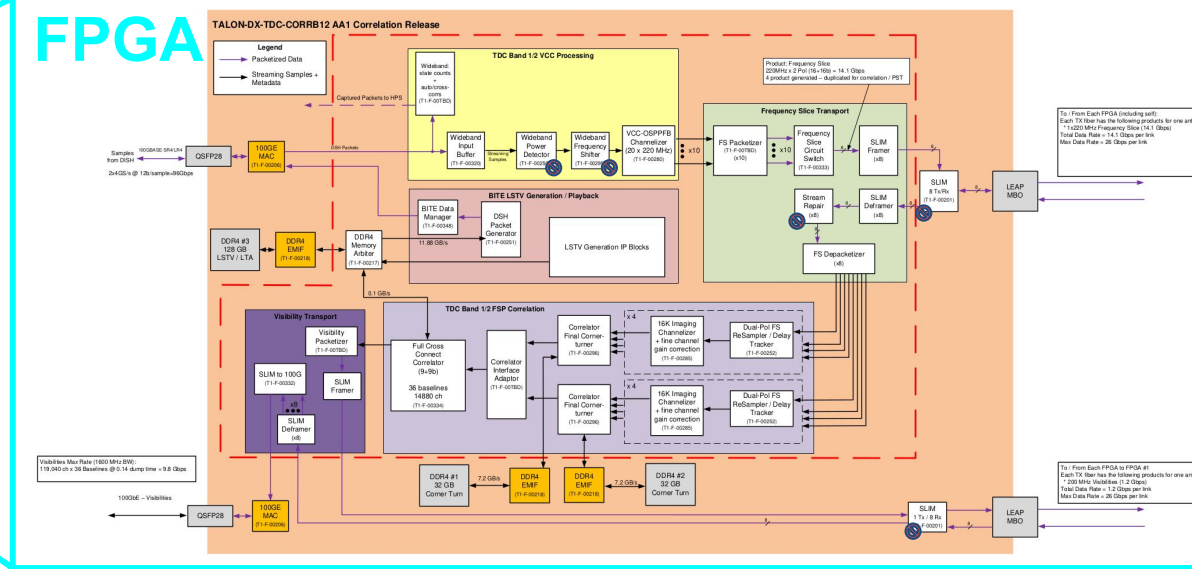
“DELL”  
Server

Commands  
(1GbE)

Visibilities  
(SPEAD 100GbE)



FPGA



# Demo ...



# Questions/Discussion

